**LAB SCHEDULE**

**Name of the dept. : ECE class : IV, ECE-A**

**Lab name : DSP LAB year : 2015-16**

**LIST OF EXPERIMENTS**

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Sl.No | Date & Session | Ex.1 | Ex.2 | Ex.3 | Ex.4 | Ex.5 | Ex.6 | Ex.7 | Ex.8 | Ex.9 | Ex.10 |
| 1 | 28.1.16(AN) | B1,B6 | B2,B7 | B3,B8 | B4,B9 | B5,B10 |  |  |  |  |  |
| 2 | 11.2.16(AN) | B11,B16 | B12,B17 | B13,B18 | B14,B19 | B15,B20 |  |  |  |  |  |
| 3 | 25.2.16(AN) | B2,B7 | B3,B8 | B4,B9 | B5,B10 | B1,B6 |  |  |  |  |  |
| 4 | 3.3.16(AN) | B12,B17 | B13,B18 | B14,B19 | B15,B20 | B11,B16 |  |  |  |  |  |
| 5 | 10.3.16(AN) | B3,B8 | B4,B9 | B5,B10 | B1,B6 | B2,B7 |  |  |  |  |  |
| 6 | 17.3.16(AN) |  |  |  |  |  | B1,B6 | B2,B7 | B3,B8 | B4,B9 | B5,B10 |
| 7 | 24.3.16(AN) |  |  |  |  |  | B11,B16 | B12,B17 | B13,B18 | B14,B19 | B15,B20 |
| 8 | 31.3.16(AN) |  |  |  |  |  | B2,B7 | B3,B8 | B4,B9 | B5,B10 | B1,B6 |
| 9 | 7.4.16(AN) |  |  |  |  |  | B12,B17 | B13,B18 | B14,B19 | B15,B20 | B11,B16 |
| 10 | 21.4.16(AN) |  |  |  |  |  | B3,B8 | B4,B9 | B5,B10 | B1,B6 | B2,B7 |
| 11 | 20.1.16(FN) | B13,B18 | B14,B19 | B15,B20 | B11,B16 | B12,B17 |  |  |  |  |  |
| 12 | 27.1.16(FN) | B4,B9 | B5,B10 | B1,B6 | B2,B7 | B3,B8 |  |  |  |  |  |
| 13 | 10.2.16(FN) | B14,B19 | B15,B20 | B11,B16 | B12,B17 | B13,B18 |  |  |  |  |  |
| 14 | 24.2.16(FN) | B5,B10 | B1,B6 | B2,B7 | B3,B8 | B4,B9 |  |  |  |  |  |
| 15 | 2.3.16(FN) | B15,B20 | B11,B16 | B12,B17 | B13,B18 | B14,B19 |  |  |  |  |  |
| 16 | 9.3.16(FN) |  |  |  |  |  | B13,B18 | B14,B19 | B15,B20 | B11,B16 | B12,B17 |
| 17 | 16.3.16(FN) |  |  |  |  |  | B4,B9 | B5,B10 | B1,B6 | B2,B7 | B3,B8 |
| 18 | 6.4.16(FN) |  |  |  |  |  | B14,B19 | B15,B20 | B11,B16 | B12,B17 | B13,B18 |
| 19 | 13.4.16(FN) |  |  |  |  |  | B5,B10 | B1,B6 | B2,B7 | B3,B8 | B4,B9 |
| 20 | 20.4.16(FN) |  |  |  |  |  | B15,B20 | B11,B16 | B12,B17 | B13,B18 | B14,B19 |
| 21 | 28.4.16 | B1-B10 Internal exam | | | | |  |  |  |  |  |
| 22 | 4.5.16(FN) |  |  |  |  |  | B11-B20 internal exam | | | | |

1. To study the architecture of DSP chips – TMS 320C 5X/6X instructions.

2. To verify linear convolution.

3. To verify the circular convolution.

4. To design FIR filter (LP/HP) using windowing technique. i. Using rectangular window. ii. Using Hamming window. iii. Using Kaiser Window.

5. To implement IIR filter (LP/HP) on DSP processors.

6. N – point FFT algorithm.

7. MATLAB program to generate sum of sinusoidal signals.

8. MATLAB program to find frequency response of analog LP/HP filters.

9. To compute power density spectrum of a sequence.

10. To find the FFT of given 1 – D signal and plot.